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United States Patent [19]

Gupta et al.

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[54] **AUTOMATED PENETRATION ANALYSIS SYSTEM AND METHOD**

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[51] **Int. Cl.**^{6} **H04L 9/00; G06F 13/00**

[52] **U.S. Cl.** **395/186**; 364/DIG. 1; 364/286.4; 364/DIG. 2; 364/918.7; 395/600; 380/4

[58] **Field of Search** 364/580, 570, 364/918.7, 949.81, 286.4, 286.5, 286.6, 419; 395/50, 51, 61, 911, 912, 913, 914, 915, 916, 917, 918, 725; 340/825.31, 825.34; 380/4

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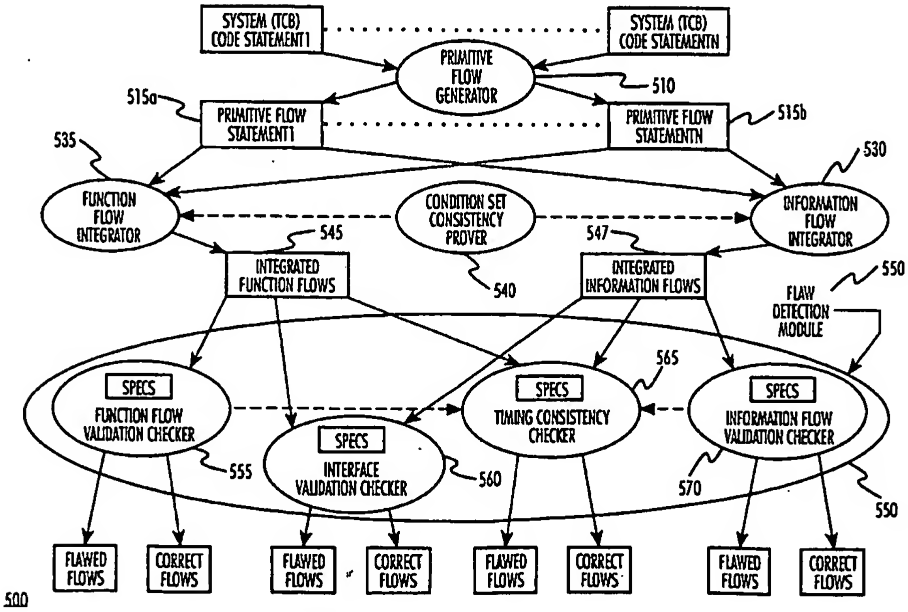
[57] **ABSTRACT**

The present invention provides a penetration-analysis method, which (1) provides a systematic approach to penetration analysis, (2) enables the verification of penetration-resistance properties, and (3) is amenable to automation. An Automated Penetration Analysis (APA) tool is provided, to support the penetration analysis method. The penetration-analysis system and method is based on a theory of penetration-resistant computer systems, a model of penetration analysis, and a unified representation of penetration patterns. The theory consists of the Hypothesis of Penetration-Resistant Systems and a set of design properties that characterize resistance to penetration. The penetration-analysis model defines a set of states, a state-invariant for penetration resistance, and a set of rules that can be applied for analyzing the penetration vulnerability of a system. An interpretation of the Hypothesis of Penetration-Resistant Systems within a given system provides the Hypothesis of Penetration Patterns, which enables the present invention to define a unified representation for a large set of penetration instances as missing check patterns.

6 Claims, 31 Drawing Sheets


SYSTEM (TCB) CODE STATEMENT1
SYSTEM (TCB) CODE STATEMENTN
PRIMITIVE FLOW GENERATOR
510
515a
PRIMITIVE FLOW STATEMENT1
PRIMITIVE FLOW STATEMENTN
515b
535
530
FUNCTION FLOW INTEGRATOR
CONDITION SET CONSISTENCY PROVER
INFORMATION FLOW INTEGRATOR
545
547
INTEGRATED FUNCTION FLOWS
540
INTEGRATED INFORMATION FLOWS
550
FLAW DETECTION MODULE
565
SPECS
FUNCTION FLOW VALIDATION CHECKER
SPECS
TIMING CONSISTENCY CHECKER
SPECS
INFORMATION FLOW VALIDATION CHECKER
SPECS
INTERFACE VALIDATION CHECKER
555
560
570
550
FLAWED FLOWS
CORRECT FLOWS
FLAWED FLOWS
CORRECT FLOWS
FLAWED FLOWS
CORRECT FLOWS
FLAWED FLOWS
CORRECT FLOWS
500